Weekly progress

- Tested Kishore's A/D design with my BSIM3 code using Cadence, BMAC, Spice3 and Hspice
- Debugged model errors.
- Tested some other circuits, which BMAC can run through.
- Worked with Chris and Gus on the EKV model.

Next week plan

- Will test some other circuits to collect some simulation data.
- Will be working with Chris and Gus to debug the EKV model.
- Will refine and debug the SOI model.